



# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Result of III B.Tech II Semester (R20/R19/R16) Supplementary Examinations, Dec-2023

College name: GAYATHRI VIDYA PARISHAD COLL OF ENGG FOR WOMEN, VISAKHAPATNA:JG

Htno	Subcode	Subname	Internals	Grade	Credits
16JG1A1224	R163205A	ARTIFICIAL INTELLIGENCE	16	C	3
16JG1A1224	R1632122	WEB TECHNOLOGIES	14	D	3
17JG1A0201	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	12	F	0
17JG1A0201	R1632024	DATA STRUCTURES	13	F	0
17JG1A0202	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	9	F	0
17JG1A0471	R1632041	MICRO PROCESSORS & MICRO CONTROLLERS	18	D	3
17JG1A0471	R1632043	VLSI DESIGN	18	F	0
17JG1A04B4	R1632044	DIGITAL SIGNAL PROCESSING	20	C	3
17JG1A0584	R1632051	COMPUTER NETWORKS	20	F	0
17JG1A0584	R1632052	DATA WAREHOUSING AND MINING	11	ABSENT	0
17JG1A0584	R163205A	ARTIFICIAL INTELLIGENCE	12	ABSENT	0
17JG1A05A6	R1632052	DATA WAREHOUSING AND MINING	14	F	0
17JG1A05A6	R1632053	DESIGN AND ANALYSIS OF ALGORITHMS	14	D	3
17JG1A05A6	R1632054	SOFTWARE TESTING METHODOLOGIES	13	F	0
17JG1A05A6	R163205A	ARTIFICIAL INTELLIGENCE	12	F	0
18JG1A0222	R1632023	MICRO PROCESSORS AND MICRO CONTROLLERS	11	F	0
18JG1A0417	R163204D	BIO-MEDICAL ENGINEERING	12	F	0
18JG1A0419	R1632044	DIGITAL SIGNAL PROCESSING	23	F	0
18JG1A0419	R163204D	BIO-MEDICAL ENGINEERING	16	F	0
18JG1A04A7	R1632043	VLSI DESIGN	20	F	0
18JG1A04A7	R1632044	DIGITAL SIGNAL PROCESSING	18	F	0
18JG1A0535	R1632052	DATA WAREHOUSING AND MINING	10	F	0
18JG1A0582	R1632051	COMPUTER NETWORKS	16	C	3
18JG1A0582	R1632053	DESIGN AND ANALYSIS OF ALGORITHMS	18	D	3
18JG1A0582	R1632054	SOFTWARE TESTING METHODOLOGIES	17	D	3
18JG1A0582	R163205A	ARTIFICIAL INTELLIGENCE	13	D	3
18JG1A1212	R1632051	COMPUTER NETWORKS	12	F	0
18JG1A1212	R163205A	ARTIFICIAL INTELLIGENCE	14	F	0
18JG5A0407	R1632044	DIGITAL SIGNAL PROCESSING	18	C	3
19JG1A0419	R1932041	WIRED AND WIRELESS TRANSMISSION DEVICES	15	D	3
19JG1A0441	R1932043	DIGITAL SIGNAL PROCESSING	4	F	0
19JG1A0443	R1932042	VLSI DESIGN	11	D	3
19JG1A0443	R1932043	DIGITAL SIGNAL PROCESSING	8	F	0
19JG1A0443	R1932044	INTERNET OF THINGS	10	ABSENT	0
19JG1A0446	R1932041	WIRED AND WIRELESS TRANSMISSION DEVICES	13	D	3
19JG1A0446	R1932042	VLSI DESIGN	14	D	3
19JG1A0446	R193204B	DIGITAL IC DESIGN	9	F	0
19JG1A0448	R1932043	DIGITAL SIGNAL PROCESSING	11	F	0
19JG1A0471	R1932041	WIRED AND WIRELESS TRANSMISSION DEVICES	15	D	3
19JG1A0471	R1932044	INTERNET OF THINGS	12	D	3
19JG1A0476	R1932041	WIRED AND WIRELESS TRANSMISSION DEVICES	11	B	3
19JG1A0476	R1932042	VLSI DESIGN	13	C	3
19JG1A0476	R1932043	DIGITAL SIGNAL PROCESSING	9	D	3
19JG1A0490	R193204B	DIGITAL IC DESIGN	8	ABSENT	0
19JG1A0575	R1932051	WEB TECHNOLOGIES	20	D	3

Htno	Subcode	Subname	Internals	Grade	Credits
19JG1A1216	R193202G	RENEWABLE ENERGY SOURCES (EXCEPT EEE)	6	F	0
20JG1A0206	R2032021	MICROPROCESSORS AND MICROCONTROLLERS	10	F	0
20JG1A0210	R203205G	WEB TECHNOLOGIES	20	D	3
20JG1A0213	R2032021	MICROPROCESSORS AND MICROCONTROLLERS	7	F	0
20JG1A0213	R203205G	WEB TECHNOLOGIES	14	E	3
20JG1A0214	R2032021	MICROPROCESSORS AND MICROCONTROLLERS	8	E	3
20JG1A0214	R203205G	WEB TECHNOLOGIES	12	E	3
20JG1A0227	R2032021	MICROPROCESSORS AND MICROCONTROLLERS	10	E	3
20JG1A0227	R2032023	POWER SYSTEM ANALYSIS	9	AB	0
20JG1A0227	R203202B	ELECTRIC DRIVES	11	E	3
20JG1A0227	R203205G	WEB TECHNOLOGIES	16	AB	0
20JG1A0402	R2032042	VLSI DESIGN	25	B	3
20JG1A0402	R2032043	DIGITAL SIGNAL PROCESSING	16	E	3
20JG1A0405	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	15	F	0
20JG1A0405	R2032042	VLSI DESIGN	17	E	3
20JG1A0405	R203204A	MICROWAVE ENGINEERING	15	F	0
20JG1A0416	R2032043	DIGITAL SIGNAL PROCESSING	26	D	3
20JG1A0417	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	24	D	3
20JG1A0417	R2032043	DIGITAL SIGNAL PROCESSING	21	F	0
20JG1A0418	R2032043	DIGITAL SIGNAL PROCESSING	20	E	3
20JG1A0418	R203204B	MOBILE & CELLULAR COMMUNICATION	16	E	3
20JG1A0420	R2032043	DIGITAL SIGNAL PROCESSING	20	D	3
20JG1A0420	R203205G	WEB TECHNOLOGIES	9	F	0
20JG1A0421	R2032043	DIGITAL SIGNAL PROCESSING	27	B	3
20JG1A0424	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	15	AB	0
20JG1A0424	R2032042	VLSI DESIGN	13	F	0
20JG1A0424	R2032043	DIGITAL SIGNAL PROCESSING	10	AB	0
20JG1A0424	R2032047	ARM BASED/AURDINO BASED PROGRAMMING	0	E	2
20JG1A0424	R203205G	WEB TECHNOLOGIES	10	F	0
20JG1A0436	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	26	C	3
20JG1A0437	R2032043	DIGITAL SIGNAL PROCESSING	27	C	3
20JG1A0445	R2032042	VLSI DESIGN	23	D	3
20JG1A0445	R2032043	DIGITAL SIGNAL PROCESSING	26	C	3
20JG1A0450	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	28	C	3
20JG1A0461	R2032043	DIGITAL SIGNAL PROCESSING	29	D	3
20JG1A0469	R2032043	DIGITAL SIGNAL PROCESSING	17	D	3
20JG1A0485	R2032043	DIGITAL SIGNAL PROCESSING	30	C	3
20JG1A0488	R2032043	DIGITAL SIGNAL PROCESSING	25	D	3
20JG1A0490	R2032042	VLSI DESIGN	23	D	3
20JG1A0490	R2032043	DIGITAL SIGNAL PROCESSING	25	E	3
20JG1A0495	R2032043	DIGITAL SIGNAL PROCESSING	11	AB	0
20JG1A0495	R203205G	WEB TECHNOLOGIES	6	AB	0
20JG1A0497	R2032043	DIGITAL SIGNAL PROCESSING	21	D	3
20JG1A0498	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	24	F	0
20JG1A0498	R2032042	VLSI DESIGN	26	D	3
20JG1A04A4	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	14	F	0
20JG1A04A4	R2032042	VLSI DESIGN	14	F	0
20JG1A04A4	R2032043	DIGITAL SIGNAL PROCESSING	10	F	0
20JG1A04A4	R203204A	MICROWAVE ENGINEERING	14	F	0
20JG1A04A4	R203205G	WEB TECHNOLOGIES	4	AB	0
20JG1A04A5	R2032041	MICROPROCESSOR AND MICROCONTROLLERS	12	F	0

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20JG1A04A5	R2032043	DIGITAL SIGNAL PROCESSING	13	F	0
20JG1A0501	R203205B	BIG DATA ANALYTICS	18	D	3
20JG1A0507	R203205B	BIG DATA ANALYTICS	21	E	3
20JG1A0508	R2032052	COMPILER DESIGN	16	F	0
20JG1A0508	R203205B	BIG DATA ANALYTICS	15	E	3
20JG1A0508	R203205E	MEAN STACK DEVELOPMENT	20	F	0
20JG1A0521	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	16	E	3
20JG1A0521	R203205E	MEAN STACK DEVELOPMENT	21	F	0
20JG1A0526	R203205B	BIG DATA ANALYTICS	19	D	3
20JG1A0528	R2032052	COMPILER DESIGN	12	F	0
20JG1A0541	R2032051	MACHINE LEARNING	19	D	3
20JG1A0546	R2032051	MACHINE LEARNING	23	C	3
20JG1A0546	R2032052	COMPILER DESIGN	14	F	0
20JG1A0546	R203205E	MEAN STACK DEVELOPMENT	19	F	0
20JG1A0549	R203205E	MEAN STACK DEVELOPMENT	22	F	0
20JG1A0551	R2032051	MACHINE LEARNING	23	E	3
20JG1A0551	R203205B	BIG DATA ANALYTICS	17	E	3
20JG1A0563	R203205B	BIG DATA ANALYTICS	19	E	3
20JG1A0582	R2032051	MACHINE LEARNING	22	C	3
20JG1A0583	R2032051	MACHINE LEARNING	13	F	0
20JG1A0583	R2032052	COMPILER DESIGN	15	F	0
20JG1A0583	R2032053	CRYPTOGRAPHY AND NETWORK SECURITY	11	F	0
20JG1A0583	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	8	F	0
20JG1A0583	R203205E	MEAN STACK DEVELOPMENT	16	F	0
20JG1A0593	R2032051	MACHINE LEARNING	15	E	3
20JG1A0593	R203205C	OBJECT ORIENTED ANALYSIS AND DESIGN	20	D	3
20JG1A05A1	R203205B	BIG DATA ANALYTICS	23	D	3
20JG1A05C4	R2032052	COMPILER DESIGN	24	F	0
20JG1A1208	R203204L	IOT AND APPLICATIONS	13	F	0
20JG1A1208	R2032053	CRYPTOGRAPHY AND NETWORK SECURITY	11	F	0
20JG1A1208	R203205A	MOBILE COMPUTING	14	F	0
20JG1A1208	R2032121	BIG DATA ANALYTICS	15	E	3
20JG1A1209	R203204L	IOT AND APPLICATIONS	9	F	0
20JG1A1221	R203204L	IOT AND APPLICATIONS	9	F	0
20JG1A1221	R2032051	MACHINE LEARNING	12	AB	0
20JG1A1221	R2032053	CRYPTOGRAPHY AND NETWORK SECURITY	8	F	0
20JG1A1221	R203205A	MOBILE COMPUTING	11	F	0
20JG1A1221	R2032121	BIG DATA ANALYTICS	18	E	3
20JG1A1224	R203204L	IOT AND APPLICATIONS	14	F	0
20JG1A1235	R203205A	MOBILE COMPUTING	19	F	0
20JG1A4201	R203205E	MEAN STACK DEVELOPMENT	5	AB	0
20JG1A4226	R203205E	MEAN STACK DEVELOPMENT	23	D	3
20JG1A4230	R203205E	MEAN STACK DEVELOPMENT	20	E	3
20JG1A4230	R2032422	DEEP LEARNING	17	E	3
21JG5A0210	R203205G	WEB TECHNOLOGIES	17	D	3
21JG5A0412	R2032043	DIGITAL SIGNAL PROCESSING	15	F	0
21JG5A0509	R2032052	COMPILER DESIGN	17	F	0

\*\*Note:1)[Last Date to apply for Recounting/Revaluation/Challenge Revaluation is : 11-03-2024 ]

\*\* Note:\*\*

\* -1 in the filed of externals indicates student is absent for the respective subject.

\* -2 in the filed of externals or ( WH ) in grade indicates student result Withheld for the respective subject.

\* -3 in the filed of externals indicates student involved in Malpractice for the respective subject.



Date:01.03.2024

Controller of Examinations(UG)